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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	09/724,974 / 7823
				Filing Date	November 28, 2000
				First Named Inventor	Stephen M. Trimberger
				Art Unit	2137
				Examiner Name	Minh Dieu T. Nguyen
Sheet	1	of	1	Attorney Docket Number	X-805-3 US



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ² -Number ³ Kind Code ⁴ (if known)			

**Examiner
Signature**

Nguyễn Minh

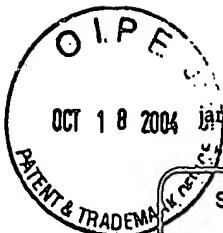
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1/27/06

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 3

Sheet 1 of 5

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
MM	US- 5,838,901		08-05-96	Curd et al.	—
	US- 6,323,679		11-27-01	Robertson et al.	—
	US- 6,246,768		06-12-01	Kim	—
	US- 09/724,865		11-28-00	Trimberger et al.	—
	US- 09/724,652		11-28-00	Pang et al.	—
	US- 6,366,117		04-02-02	Pang et al.	—
	US- 6,441,641		08-27-02	Pang et al.	—
	US- 09/724,975		11-28-00	Pang et al.	—
	US- 09/724,873		11-28-00	Pang et al.	—
	US- 09/724,734		11-28-00	Trimberger et al.	—
	US- 09/724,972		11-28-00	Trimberger et al.	—
	US- 6,560,709		05-06-03	Galovich	—
	US- 6,118,869		09-12-00	Kelem et al.	—
	US- 6,212,639 B1		04-03-01	Erickson et al.	—
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	US- 5,081,675		01-14-92	Kittirutsunetorn	—
	US- 5,341,034		08-23-94	Matthews	—
	US- 5,121,359		06-09-92	Steele	—
	US- 5,388,157		02-07-95	Austin	—
	US- 6,654,889		11-25-03	Trimberger	—

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Sheet 2 of 3

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Sheet 3 of 3

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OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MOR		ARM; Versatile FPGA and PLD configuration images, update 13 Sept 04, pg 1 of 1, available at http://www.arm.com/support/downloads/info/4804.html	
Myn		XILINX, INC., News Release, September 20, 1999, pp 1-2, available at http://investor.xilinx.com/phoenix.zhtml?c=75919&p=irol-newsArticle&t=Regular&id=5214...	

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				Filing Date	November 28, 2000
				First Named Inventor	Stephen M. Trimberger
				Group Art Unit	Not Yet Known
				Examiner Name	Not Yet Known
Sheet	1	of	2	Attorney Docket Number	X-805-3 US

Examiner Signature	nguyen	Date Considered	1/27/06
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INFORMATION DISCLOSURE
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Application Number	Not Yet Known
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First Named Inventor	Stephen M. Trimberger
Group Art Unit	Not Yet Known
Examiner Name	Not Yet Known

Attorney Docket Number

X-805-3 US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MJM		Xilinx Application Note, "Configuration Issues: Power-Up, Volatility, Security, Battery Back-Up", XAPP 092, November 24, 1997 (Version 1.1).	
		Virtex Configuration Guide, published by Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124, October 9, 2000 (Version 1.0).	
		Bruce Schneier "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Second Edition, Copyright 1996, published by John Wiley & Sons, Inc.; Chapter 9, pages 193-194 and 200-203; Chapter 12, pages 265-301 and Chapter 15, pages 360-361	
		Cahners EDN ACCESS Web Page, "Cunning circuits confound crooks," October 12, 2000; pages 1-8; http://www.ednmag.com/ednmag/reg/2000/10122000/21df2.htm	
		Xilinx, Application Note, XAPP138, "Virtex FPGA Series Configuration and Readback", published October 4, 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	

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